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# Cell info

	SLC	MLC	TLC	3D MLC V-NAND	3D TLC V-NAND
Bits per cell	1	2	3	2	3
approx. P/E Cycles	100000	3000-10000	1500	6000	2000

Available Reserved Space/Block Count - total reserved space for remapping bad blocks.

Remark:

256GB MLC

256GB \* 3000 = **750 TB** total transfer (728 TB - 1200 TB in real life)

256GB TLC

256GB \* 1000 = **250 TB** total transfer (300-800 TB in real life)

Wear Leveling - Dynamic, Static

**Initial Bad Blocks**

**Later Bad Blocks** - Bad blocks that are developed during the lifespan of the flash

## Tested SSD

Name	Cell Type	Capacity	Data written
SanDisk SDSSDH2064G	MLC	64 GB	170 TB
Intel SSD 520	MLC	60 GB	181 TB
Samsung 840	TLC	120 GB	331 TB
Samsung 840	TLC	250 GB	760 TB

100 - ( 100 \* Average Erase Count / NAND max PE)

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